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Docket: 14440.01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor:	Bryan J. Root	
Appln. No.:	10/601,764	
Filing Date:	June 23, 2003	Examiner: Nguyen, Vinh P.
Title:	Probe Tile for Probing Semiconductor Wafer	Group Art Unit: 2829

AMENDMENT AND RESPONSE UNDER 37 C.F.R. 1.111

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, Virginia 22313-1450

I hereby certify that this document is being sent via First Class U. S. mail addressed to: Commissioner for Patents, P. O Box 1450, Alexandria, VA 22313-1450, on this 10 day of September, 2004.

Barbara A. Avery
Barbara A. Avery

Dear Sir:

In response to the Office Action of June 14, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.